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| QN=1 | Which of the following is not the basic component Structure of von Neumann Machine. |
| a. | Cache memory |
| b. | Main memory |
| c. | ALU |
| d | Input / output (I / O) |
| e  e. | control unit |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 2 |
| MIX CHOICES: | Yes |

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| QN=2 | Which of the following functions is not the 4 basic functions that computers have? |
| A | Data processing |
| B | Data saving |
| C | Move data |
| D | Control |
| E | Interrupt |
| ANS: | E |
| PTS: | 1 |
| CHAPTER: | 1 |
| MIX CHOICES: | Yes |

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| QN=3 | What is the following statement about the generation of computers?  "All components of a circuit are scaled into microphones. So they are all packed into one chip." |
| a. | 1 |
| b. | 2 |
| c. | 3 |
| d. | 4 |
| ANS: | C |
| PTS: | 1 |
| CHAPTER: | 2 |
| MIX CHOICES: | Yes |

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| QN=4 | What do the following concepts describe?  Processor receives commands from memory  The program counter (PC) holds the address of the command to be fetched next  Processor increases PC after each fetch  The fetched command is loaded into the command register (IR). |
| a. | Fetch Cycle |
| b. | Instruction cycle |
| c. | Execute cycle |
| d. | Process cyle |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 3 |
| MIX CHOICES: | Yes |

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| QN=5 | What is the processing required for a single instruction is called? |
| a. | instruction cycle |
| b. | Instruction |
| c. | Function |
| d. | Programs |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 3 |
| MIX CHOICES: | Yes |

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| QN=6 | What is the concept of sequential access ?   1. Access must be done in a specific linear sequence 2. Memory is organized into data units called records 3. Access time is variable |
| a. | A |
| b. | B+C |
| c. | A+C |
| d. | A+B+C |
| ANS: | D |
| PTS: | 1 |
| CHAPTER: | 4 |
| MIX CHOICES: | Yes |

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| QN=7 | What is the content of Direct Access?   1. Related to a shared read-write mechanism 2. Individual blocks or records have a unique address based on actual location 3. Access time is variable 4. all concepts |
| a. | A |
| b. | B |
| c. | C |
| d. | D |
| ANS: | D |
| PTS: | 1 |
| CHAPTER: | 4 |
| MIX CHOICES: | Yes |

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| QN=8 | Which of the following describes Random Access ?   1. Each address location in memory has a unique mechanism 2. Access time to a location does not depend on the previous series of visits 3. Any position can be selected randomly |
| a. | A |
| b. | A+B+C |
| c. | A+C |
| d. | A+B |
| ANS: | B |
| PTS: | 1 |
| CHAPTER: | 4 |
| MIX CHOICES: | Yes |

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| QN=9 | Which of the following statements does not describe Cache memory? |
| a. | A Small size, expensive, high speed access |
| b. | Big size, cheaper, lower speed memory |
| c. | This memory is located between the CPU and RAM |
| d. | A memory area that improves the access speed between CPU and RAM |
| ANS: | B |
| PTS: | 1 |
| CHAPTER: | 4 |
| MIX CHOICES: | Yes |

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| QN=10 | Which of the following does not describe DRAM? |
| a. | Binary values are stored using traditional flip-flop logic gate configurations |
| b. | The presence or absence of a charge in the capacitor is interpreted as binary 1 or 0 |
| c. | Require periodic renewal of fees to maintain data storage |
| d. | Created by cells that store data in the form of charges on the capacitor |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 5 |
| MIX CHOICES: | Yes |

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| QN=11 | Which of the following describes SRAM?   1. Digital device that uses the same logic elements used in the processor 2. Binary values are stored using traditional flip-flop logic gate configurations 3. Will hold its data as long as power is supplied to it 4. Require periodic renewal of fees to maintain data storage |
| a. | A+B+C |
| b. | A+B+C+D |
| c. | B+C+D |
| d. | C+D |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 5 |
| MIX CHOICES: | Yes |

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| QN=12 | What are the following statements describing objects?  Devices that contain permanent data formats cannot be changed or added. Without the energy needed to maintain the bit value in memory. Real data is connected to the chip as part of the production process. |
| a. | ROM |
| b. | RAM |
| c. | flash memory |
| d. | HDD |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 5 |
| MIX CHOICES: | Yes |

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| QN=13 | Which statement is not a description of EPROM ? |
| a. | Programmable read-only memory can be deleted |
| b. | The deletion process can be performed multiple times |
| c. | The price is expensive but it has the advantage of being able to update much |
| d. | Using the power erase technology, does not provide the function to delete the byte level |
| ANS: | D |
| PTS: | 1 |
| CHAPTER: | 6 |
| MIX CHOICES: | Yes |

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| QN=14 | The object is a collection of physical drives that the operating system sees as a single logical drive called . |
| a. | RAID |
| b. | Cache memory |
| c. | flash memory |
| d. | virtual memory |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 6 |
| MIX CHOICES: | Yes |

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| QN=15 | Which of the following is not the advantage of SSD versus hard drive? |
| a. | High performance input / output operation per second (IOPS) |
| b. | Durability / Longer life |
| c. | Larger storage size |
| d. | Lower power consumption, the ability to run smoothly and cooler. |
| ANS: | C |
| PTS: | 1 |
| CHAPTER: | 6 |
| MIX CHOICES: | Yes |

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| QN=16 | What is programmed I / O ?   1. Data is exchanged between processor modules and I / O 2. The processor executes a program that allows it to directly control I / O operations 3. When the processor issues an instruction, it must wait until the I / O operation is completed 4. The processor directly calls the I / O device |
| a. | A+B+C+D |
| b. | A+B+C |
| c. | A+D |
| d. | A+B+D |
| ANS: | B |
| PTS: | 1 |
| CHAPTER: | 7 |
| MIX CHOICES: | Yes |

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| QN=17 | Which of the following statements describes the system in the computer?   * Used when users interact directly with the computer * Processing time is shared between multiple users * Many users simultaneously access the system through the terminal |
| a. | Interactive system |
| b. | Batch system  SPACE |
| c. | Time-sharing system |
| d. | Memory Management |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 8 |
| MIX CHOICES: | Yes |

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| QN=18 | Which of the following statements describes the system?   * The most important system program * Mask the details of the hardware from the programmer and give the programmer a convenient interface to use the system |
| a. | OS |
| b. | Interactive system |
| c. | Drive |
| d. | Batch system |
| ANS: | A |
| PTS: | 1 |
| CHAPTER: | 8 |
| MIX CHOICES: | Yes |

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| QN=19 | Which of the following describes the operation of the Batch system   1. Opposite of interactive 2. The user’s program is batched together with programs from other users and submitted by a computer operator 3. After the program is completed results are printed out for the user 4. Used when users interact directly with the computer |
| a. | A+B+C+D |
| b. | A+B+C |
| c. | A+C |
| d. | D+A |
| ANS: | B |
| PTS: | 1 |
| CHAPTER: | 8 |
| MIX CHOICES: | Yes |

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| QN=20 | Which of the following statements is Medium-term Planning? |
| a. | The decision as to which process's pending I/O request shall be handled by an available I/O device |
| b. | The decision to add to the number of processes that are partially or fully in main memory |
| c. | The decision as to which available process will be executed by the processor |
| d. | The decision to add which programs to the system for processes |
| ANS: | B |
| PTS: | 1 |
| CHAPTER: | 8 |
| MIX CHOICES: | Yes |